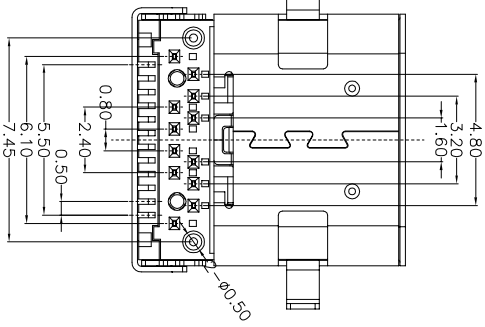


RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)  
TOLERANCE FOR PCB LAYOUT IS ±0.05



- NOTES:  
1. MATERIAL:  
MOLDING: LCP U94 V-0  
CONTACT: COPPER ALLOY,  
GOLD PLATED ON CONTACT AREA, 100u"  
Min TIN (LEAD FREE) ON SOLDER AREA,  
SHELL: SUS304-H1=0.30±0.03mm  
50u" NICKEL PLATING OVER ALL.  
SHILD: SUS304-H1=0.12±0.03mm  
2. MECHANICAL:  
INSERTION: 5~20N,  
EXTRACTION: 8~20N,  
DURABILITY: 10000 CYCLES  
3. ELECTRICAL:  
CURRENT: 5A MIN FOR OTHER,  
0.25A MIN FOR VBUS,  
VOLTAGE: 5VAC MAX  
WITHSTANDING VOLTAGE: 100V AC,  
CONTACT RESISTANCE: 40mΩ MAX,  
INSULATION RESISTANCE: 100MΩ MIN,  
4. ENVIRONMENTAL  
TEMPERATURE RANGE -55°C ~ +85°C

Pin Name	Description	Pin Name	Description
A1 (GND)	Ground return	B12 (GND)	Ground return
A2 (SSTW)	Positive half of First differential pair	B11 (SSTW)	Positive half of First differential pair
A3 (SSTW)	Negative half of First differential pair	B10 (SSTW)	Negative half of First differential pair
A4 (Vas)	Bus Power	B9 (Vas)	Bus Power
A5 (CCI)	Control separation Channel	B8 (CCI)	Control separation Channel
A6 (Dn)	Positive half of the USB 2.0 differential pair-Position 1	B7 (Dn)	Positive half of the USB 2.0 differential pair-Position 1
A7 (Dm)	Negative half of the USB 2.0 differential pair-Position 1	B6 (Dm)	Negative half of the USB 2.0 differential pair-Position 1
A8 (RH1)	Reserved for future use (RH1)	B5 (RH1)	Reserved for future use (RH1)
A9 (Vas)	Bus Power	B4 (Vas)	Bus Power
A10 (SSRN2)	Negative half of second SuperSpeed RX differential pair	B3 (SSRN2)	Negative half of second SuperSpeed RX differential pair
A11 (SSRN2)	Positive half of second SuperSpeed RX differential pair	B2 (SSRN2)	Positive half of second SuperSpeed RX differential pair
A12 (GND)	Ground return	B1 (GND)	Ground return

**CSCONN**  
PART NO.(INTENDED USE)  
CUS31121001002

UNITS: mm  
GENERAL TOLERANCE  
X. ° ±5° 0. X±0.25  
X. 1±2° 0. XX±0.2  
XX. ±1° 0. XXX±0.1

APPD: Sk11a  
CHKD: Sk11a  
DR: Kitty

TITLE: USB TYPE C FEMALE L=10.0MM 沉板式  
DWG NO. USB  
SCALE 1:1  
SHEET 1 OF 1  
REV A

